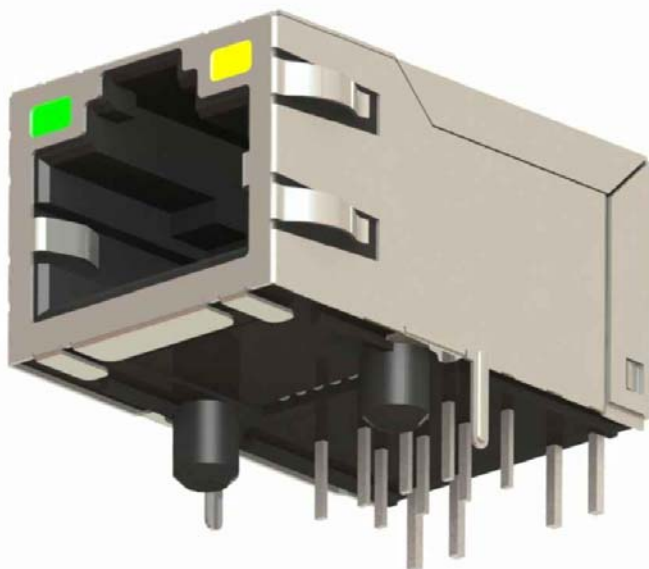


◆ Right Angle 10/100M Integrated Magnetics



Ordering Information:

RJR26-0 8 A 2 1 3

—

1.Options

	LED	Length Pin	EMI Finger	Schematic
1		2.80		Option 1
2	Green/Yellow	3.18	6 Number	Option 2
3				Option 1
4	Green/Green			
5	Green/Yellow		4 Number	Option 2
6	W/O		6 Number	Option 3
7	Green/Green			W/O Shell
8			W/O	
9				

Physical:

- ◆ Housing
 - High Temp Thermoplastic, UL94V-0
 - Color: Black
- ◆ Terminal
 - Copper Alloy
- ◆ Terminal Plating
 - Gold Plating on Contact Area
 - Tin Plating on Solder Tails
 - Nickel Underplating Overall
- ◆ Shell
 - Copper Alloy
- ◆ Shell Plating
 - Tin Plating on Solder Tails
 - Nickel Underplating Overall

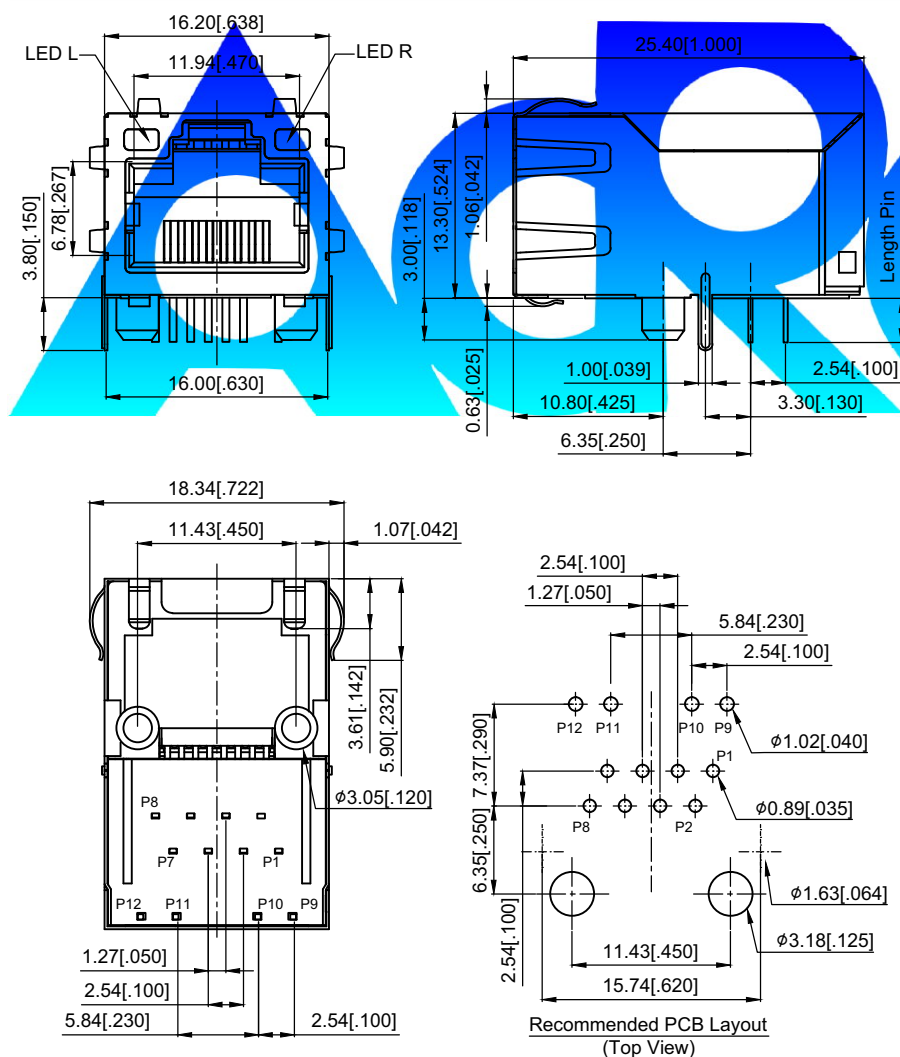
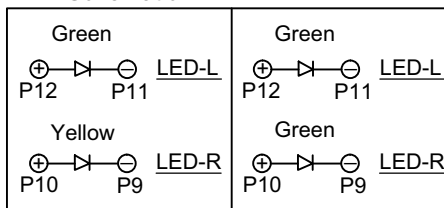
Mechanical Specifications:

- ◆ Mate Force
35N Max.
- ◆ Durability
750 Cycles
- ◆ Operating Temperature Rating
-40°C to +85°C

Electrical Specifications:

- ◆ Rated Voltage
150V RMS AC
- ◆ Rated Current
1.5A Max.
- ◆ Contact Resistance
20 Milliohms Max. Initial
- ◆ Dielectric Withstanding Voltage
1000V AC Min for 1 Minute
- ◆ Insulation Resistance
500 Megohms Min.

LED Schematic



◆ Right Angle 10/100M Integrated Magnetics

Magnetic Electrical Specifications:

◆ HI-POT

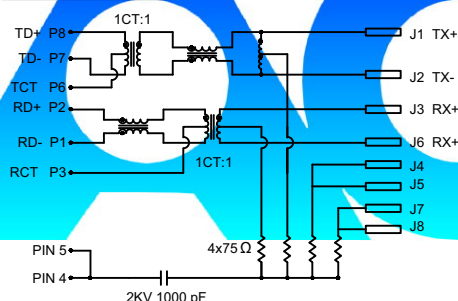
Insertion Loss:@1-100 MHz -1.0dB MAX.
Return Loss:@1-30 MHz -18dB MIN. Load 100 Ohm
@30-60 MHz -15dB MIN. Load 100 Ohm
@60-80 MHz -11dB MIN. Load 100 Ohm
Cross Talk:@1-100MHz -30dB MIN.
CMRR:@1-100MHz -30dB MIN.

◆ Withstand Voltage:

(J1~J2:P8~P7)(J3~J6:P2~P1):2000VDC,1mA,1SEC
J to LED & Shield:500VDC,1mA,1SEC
P to LED & Shield:500VDC,1mA,1SEC

◆ LCR Test:

DCR			Turns Ratio(@100KHz,0.1V)			
Pin	MAX. Ω	MIN. Ω	Pin	Standard Ratio	MAX.	MIN.
J1~J2	0.70	0.15	P8~P7 to J1~J2	1:1	2%	-2%
J3~J6	1.0	0.20	P2~P1 to J3~J6	1:1	2%	-2%
P8~P6	0.55	0.10	P8~P6 to P6~P7	0.5:0.5	2%	-2%
P6~P7	0.55	0.10	P2~P3 to P3~P1	0.5:0.5	2%	-2%
P8~P7	1.0	0.20	Inductance(100KHz,0.1V 8mA DC BISA)			
P2~P3	0.75	0.20	Pin	MIN.		
P3~P1	0.75	0.20	P8~P7	380uH		
P2~P1	1.3	0.5	J3~J6	380uH		
J1~J4	160	140	Leakage Inductance(@100KHz,0.1V)			
J2~J5	160	140	Pin	MAX.		
J3~J7	160	140	P8~P7(Short J1~J2)	0.5uH		
J6~J8	160	140	J3~J6(Short P2~P1)	0.5uH		
Capacitance(1KHz,1V)			Phase(P8,P2,J1,J3)			
Pin	MAX. pF	MIN. pF	P8 to J1	P2 to J3		
J4 to P4	1200	800	P7 to J2	P1 to J6		
J7 to P5	1200	800	Open Circuit			
P3 to P4		P5 to P6				



Option 1

◆ HI-POT

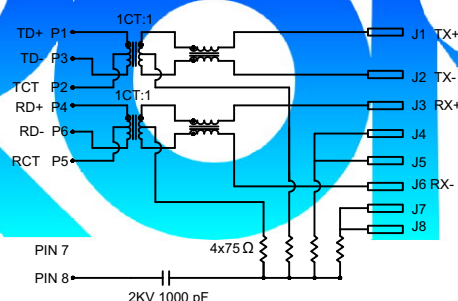
Insertion Loss:@1-100 MHz -1.1dB MAX.
Return Loss:@1-30 MHz -18dB MIN. Load 100 Ohm
@30-80 MHz -12dB MIN. Load 100 Ohm
Cross Talk:@1-100MHz -30dB MIN.
CMRR:@1-100MHz -35dB MIN.

◆ Withstand Voltage:

(J1~J2:P1~P3)(J3~J6:P4~P6):2000VDC,1mA,1SEC
J to LED & Shield:500VDC,1mA,1SEC
P to LED & Shield:500VDC,1mA,1SEC

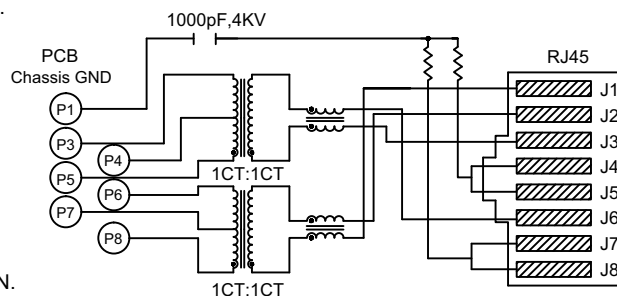
◆ LCR Test:

DCR			Turns Ratio(@100KHz,0.1V)			
Pin	MAX. Ω	MIN. Ω	Pin	Standard Ratio	MAX.	MIN.
J1~J2	1.30	0.50	P1~P3 to J1~J2	1:1	2%	-2%
J3~J6	1.30	0.50	P4~P6 to J3~J6	1:1	2%	-2%
P1~P2	0.60	0.10	P1~P2 to P2~P3	0.5:0.5	2%	-2%
P2~P3	0.60	0.10	P4~P5 to P5~P6	0.5:0.5	2%	-2%
P1~P3	1.0	0.20	Inductance(100KHz,0.1V 8mA DC BISA)			
P4~P5	0.60	0.10	Pin	MIN.		
P5~P6	0.60	0.10	P1~P3	380uH		
P4~P6	1.0	0.2	P4~P6	380uH		
J1~J4	160	140	Leakage Inductance(@100KHz,0.1V)			
J2~J5	160	140	Pin	MAX.		
J3~J7	160	140	P1~P3(Short J1~J2)	0.5uH		
J6~J8	160	140	P4~P6(Short J3~J6)	0.5uH		
Capacitance(1KHz,1V)			Phase(P1,P4,J1,J3)			
Pin	MAX. pF	MIN. pF	P1 to J1	P4 to J3		
J4 to P8	1200	800	P3 to J2	P6 to J6		
J7 to P8	1200	800	Open Circuit			
			P3 to P4	P7 to P8		
			P6 to P7	P6 to P8		



Option 2

- ◆ TURNS RATIO:(P5-P4-P3):(J3-J6):1CT:1CT
(P8-P7-P6):(J1-J2):1CT:1CT
- ◆ INDUCTANCE:(P8-P6):350uH MIN. @0.1V,100KHz,8mA DC Bias.
(P3-P5):350uH MIN. @0.1V,100KHz,8mA DC Bias.
- ◆ LEAKAGE INDUCTANCE:P5-P3(WITH J6 AND J3 SHORT):0.3 MAX.@1MHz.
P8-P6(WITH J2 AND J1 SHORT):0.3 MAX.@1MHz.
- ◆ INTERWINNING CAPACITANCE:(P5,P4,P3) TO (J6,J3):30pf MAX @1MHz.
(P8,P7,P6) TO (J2,J1):30pf MAX @1MHz.
- ◆ DC RESISTANCE:(J6-J3)=(J2-J1):1.2 ohms Max.
- ◆ RETURN LOSS:(P5-P3)=100 OHMS(P8-P6)=100 OHM REF.
1MHz TO 30MHz:18db min.:18dB MIN.
30MHz TO 80MHz:12db min.:12dB MIN.
NOTE:100 OHMS CONNECTED TO (J2-J1) TO (J6-J3).
- ◆ HI-POT:(J1,J2) TO (P8,P6):1500 VAC.
(J3,J6) TO (P5,P3):1500 VAC.
- ◆ INSERTION LOSS:RS=RL=100 ohms:1.1 dB MAX.
100KHz TO 100MHz.
- ◆ CROSS TALK:1-100 MHz:30 dB MIN.
- ◆ COMMON TO COMMON MODE REJECTION:1MHz TO 100MHz:35 dB MIN.



Option 3